#### **REMARKS**

This is intended as a full and complete response to the Office Action dated June 14, 2005, having a shortened statutory period for response set to expire on September 14, 2005. Please reconsider the claims pending in the application for reasons discussed below.

In the specification, paragraph [0001] has been amended to update the information regarding the related application, and paragraphs [0029], [0030] and [0042] have been amended to correct minor editorial errors.

Claims 1-30 are pending in the application. Claims 1, 3, 5-21 and 23-30 remain pending following entry of this response. Claims 1, 3 and 21 have been amended. Claims 2, 4 and 22 have been cancelled. Applicants submit that the amendments do not introduce new matter.

### **Drawing Objections**

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5). Replacement sheets for FIGs. 2 and 7 are submitted herewith to correct and remove the objected to reference characters. Regarding the Examiner's objection to reference signs in the specification TD[0:3] not included in the drawings, Applicants have amended the specification to clarify that TD[0:3] is an example of signals TD[0:N] with N=3. Standard nomenclature was used in the specification to describe operation of the illustrated embodiment when these signals have exemplary values, such as b'0010' (i.e., TD[0]=0, TD[1]=0, TD[2]=1, and TD[3]=0).

For these reasons, Applicants request withdrawal of this objection.

# Claim Rejections - 35 USC § 112

Claims 1-30 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. As described above, Replacements Sheets have been submitted for FIGs. 2 and 7, while standard nomenclature (e.g., TD[0:3] = b'1000') was used for specifying values of individual signals. Accordingly, Applicants request withdrawal of this rejection.

### Claim Rejections - 35 U.S.C. § 102

Claims 1-30 are, insofar as understood, rejected under 35 U.S.C. 102(e) as being anticipated by *Alexander et al.*, US Patent No. 6,809,978 (hereinafter "*Alexander*"). Applicants respectfully traverse this rejection.

"A claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference." *Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628, 631, 2 USPQ2d 1051, 1053 (Fed. Cir. 1987). "The identical invention must be shown in as complete detail as is contained in the ... claim." *Richardson v. Suzuki Motor Co.*, 868 F.2d 1226, 1236, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989). The elements must be arranged as required by the claim. *In re Bond*, 910 F.2d 831, 15 USPQ2d 1566 (Fed. Cir. 1990).

In this case *Alexander* does not disclose "each and every element as set forth in the claim". For example, *Alexander* does not disclose a mode register for supplying temperature information, or obtaining temperature information therefrom, as recited in independent claims 1 and 21. While the Examiner indicates mode registers are illustrated in FIG. 6 of *Alexander*, there is no teaching such mode registers include temperature information and, therefore, certainly no teaching temperature information from a mode register is used to vary internally generated voltages, as claimed.

Further, *Alexander* does not disclose generating, from a supply voltage, a boosted voltage greater than the supply voltage and varying the level of the boosted voltage based on temperature information, as recited in independent claims 11 and 25. As set forth in col. 4, lines 2-13 of *Alexander*, the input to an internal voltage generator is changed, so that the internal voltage generator generates a voltage such that the chip network does not see a change in the level of power being supplied to it by the internal voltage generator. Ideally, no change in power will be encountered by the chip network despite changes in temperature. Thus, Alexander is directed towards compensating internally generated voltages for changes in temperature so that the internally generated voltages are, ideally, *not varied but rather held constant*.

Therefore, the claims are believed to be allowable, and allowance of the claims is respectfully requested.

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## Conclusion

The secondary references made of record are noted. However, it is believed that the secondary references are no more pertinent to the Applicants' disclosure than the primary references cited in the office action. Therefore, Applicants believe that a detailed discussion of the secondary references is not necessary for a full and complete response to this office action.

Having addressed all issues set out in the office action, Applicants respectfully submit that the claims are in condition for allowance and respectfully request that the claims be allowed.

Respectfully submitted,

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PATENT SZS&Z Ref. No. : IO031110PUS Atty. Dkt. No. INFN/SZ0017

# IN THE DRAWINGS:

The attached sheets of drawings includes changes to Figure 2 and Figure 7.

Attachment:

**Replacement Sheets** 

**Annotated Sheets Showing Changes** 



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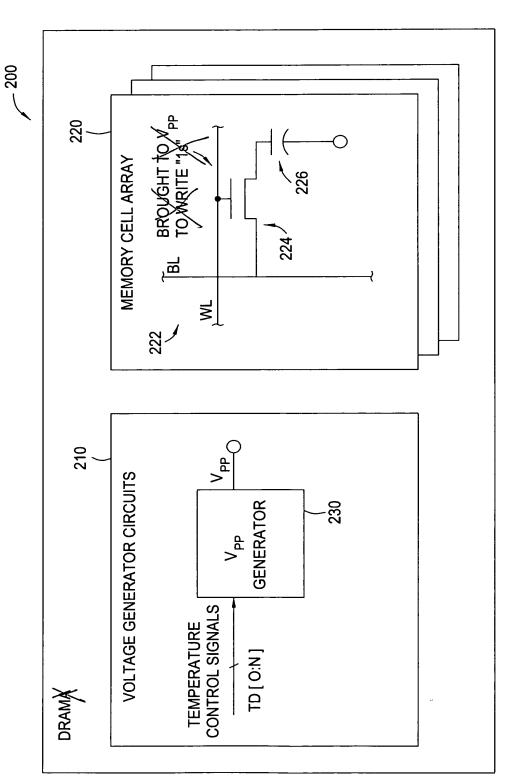
### **ANNOTATED SHEET**

ATTY DKT. NO.: J.S. SERIAL NO.: FILED: FITLE:

INFN/SZ0017
10/716,749 CONF. NO.: 6180
NOVEMBER 19, 2003
INTERNAL VOLTAGE GENERATOR WITH TEMPERATURE
CONTROL
JUNG PILL KIM ET AL. SHEET 2 OF 8

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**ANNOTATED SHEET** 

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